16th Canadian Skills Competition
16es Olympiades Canadiennes de la Formation Professionnelle et Technique
Waterloo, ON **May 20, 21, 2010** **Electronics Trade 16**

Breadboard Judges' sheet **Competitor number: _____**

Criteria	Possible Mark	Mark Awarded
Circuit Design:		
--Power supply (C values, R values)	15	
--Temperature Sensor	10	
-- Amplifier	10	
-- Slotted sensor values	10	
-- One Shot	10	
Basic function demonstrated	10	
Breadboard Layout (comparison mark)	10	
Sub Total	**75**	
Documentation:	--------	--------
-- step by step flow	20	
-- sample calculations	10	
-- measurement of values	10	
-- complete schematic	10	
Sub Total	**50**	
Bonus: complete parts list	5	
Sub Total	**50**	
Less # of hints given @ -6/hint	--------	
Grand Total	**125**	

Comments:

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Judge: _____ **Judge:** _____

Cable Assembly Judges' sheet Competitor number: _____

Criteria	Possible Mark	Mark Awarded
Colour coded (correctly)	10	
Wire stripping (-5 per error)	15	
Burned insulation (-5 per error)	10	
Cup solder (-2 per error)	18	
Flux residue (-2 per occurrence)	18	
Shorts or opens (-5per occurrence)	6	
Total	**77**	

Comments:

Judge: _____ **Judge: _____**

Fault Finding Judges' sheet **Competitor number: _____**

Criteria	Possible Mark	Mark Awarded
Fault 1-1 Steps	10	
Component	5	
Fault 1-2 Steps	10	
Component	5	
Fault 1-3 Steps	10	
Component	5	
Fault 2-1 Steps	10	
Component	5	
Fault 2-2 Steps	10	
Component	5	
Fault 2-3 Steps	10	
Component	5	
Total	**90**	

Comments:

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Judge: _____ **Judge: _____**

Reverse Engineering **Judges' sheet** **Competitor number: _____**

There are 52 parts missing from the schematic.

Criteria	Mark	Possible Mark	Mark Awarded
52 symbols	-1 per missing or incorrect symbol	52	
Connections	-1 per missing or incorrect connection	52	
Component value	-1 per incorrect or missing value	52	
Component designator	-1 per missing or incorrect designator	52	
4 Questions	5 marks each	20	
	Total	**228**	

Comments

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Judge: _____ **Judge: _____**

USB Interface Judges' sheet **Competitor number: _____**

Criteria	Possible Mark	Mark Awarded
Soldered connections, complete coverage, leads trimmed, consistency, burned flux (-5 per error)	80	
Component mounting, stress relief, clean bends (-5 per error)	43	
Demonstrated function (working)	10	
A/D conversion value graphs	10	
Minimum resolution (correctly stated)	10	
Digital input switching threshold value (correctly stated)	10	
Minimum resolution (correctly stated)	10	
Total	**173**	

Comments:

Judge: _____ **Judge: _____**